

FIG. 1

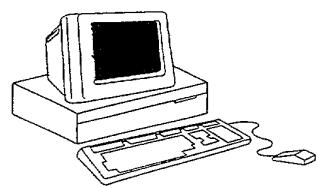
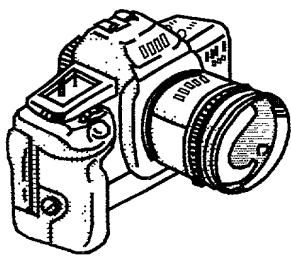
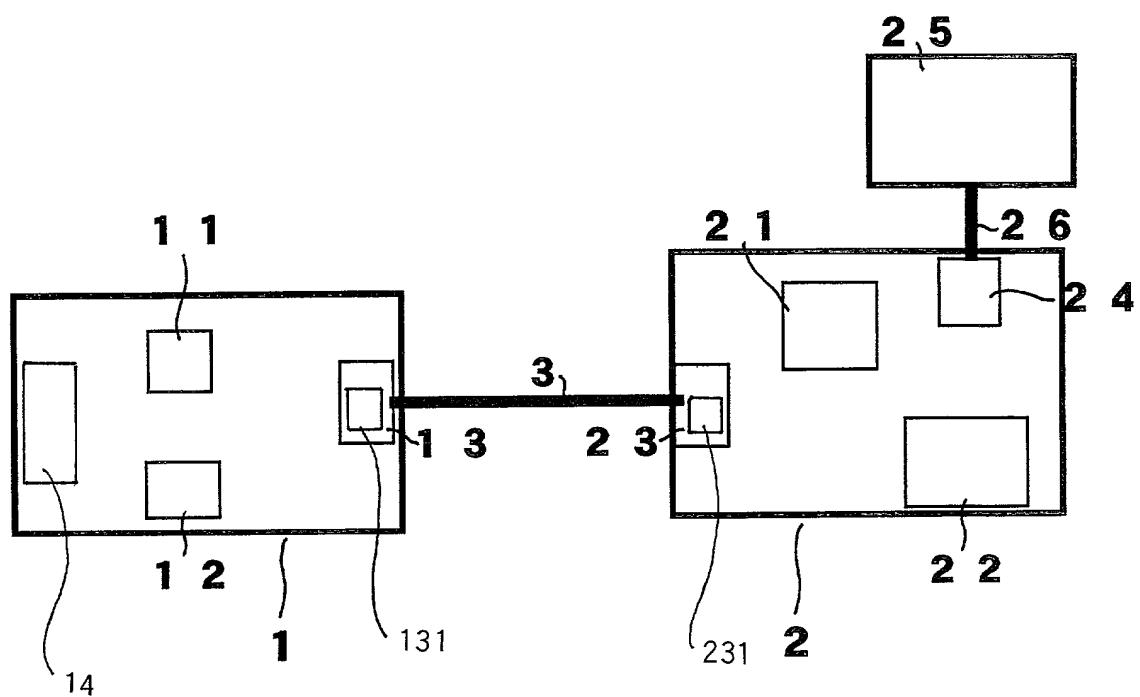


FIG. 2

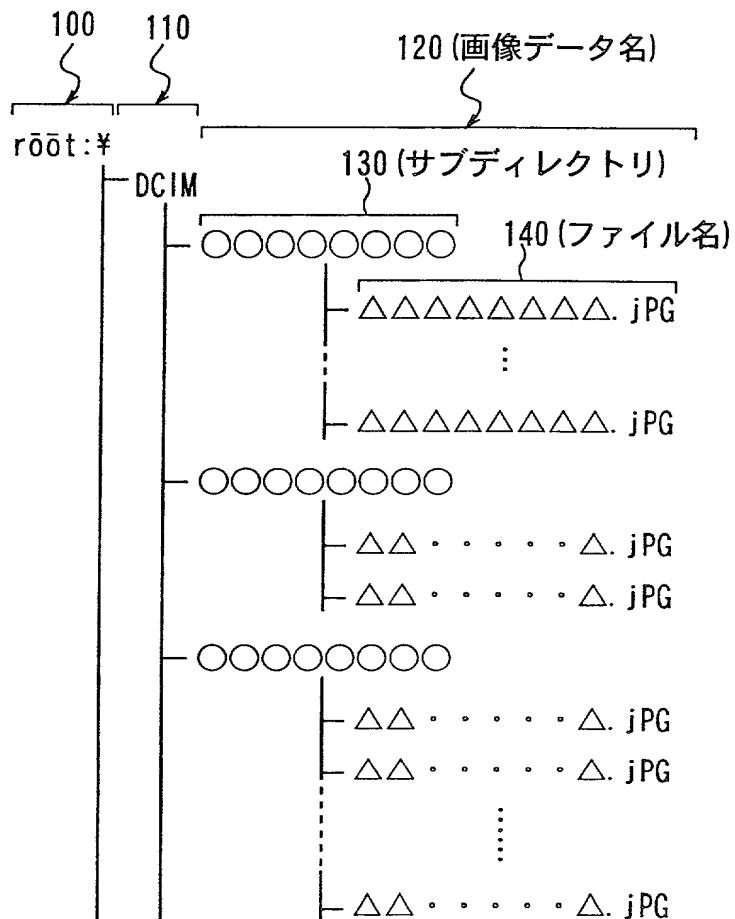


FIG. 3

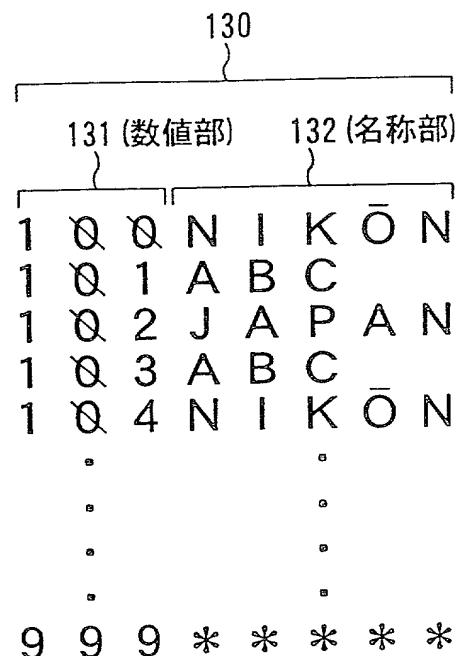


FIG. 4

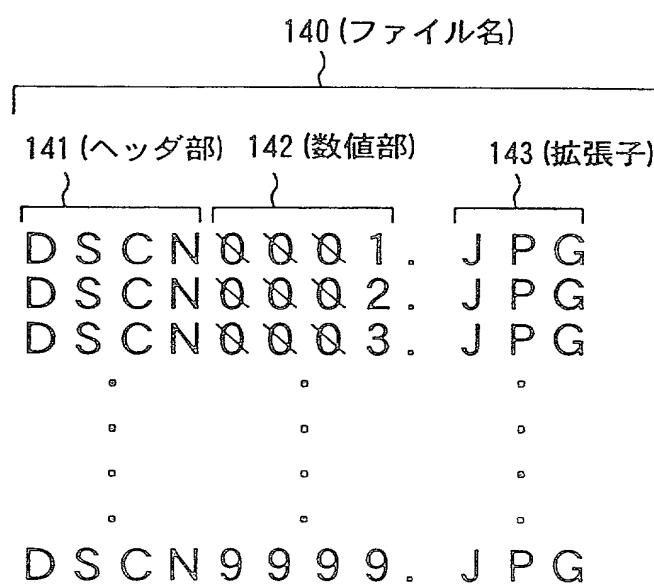


FIG. 5A

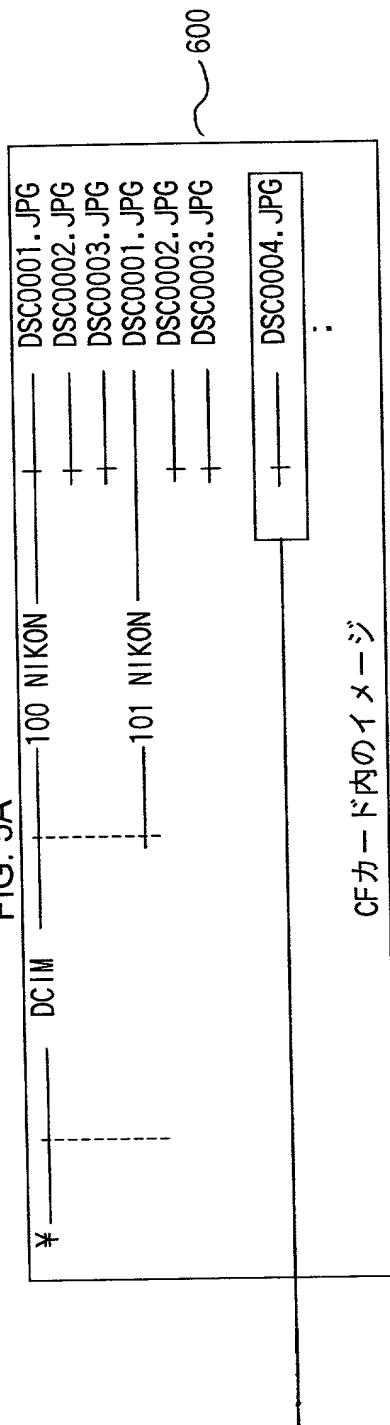


FIG. 5B

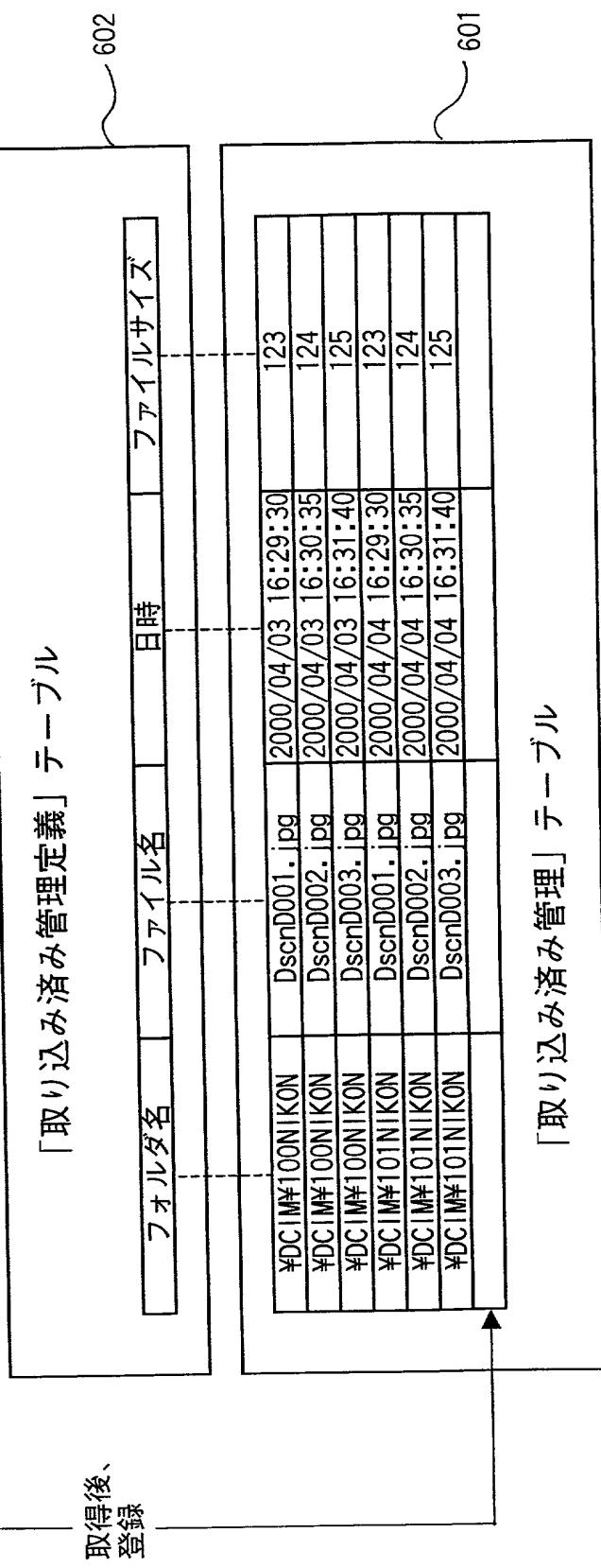


FIG. 6

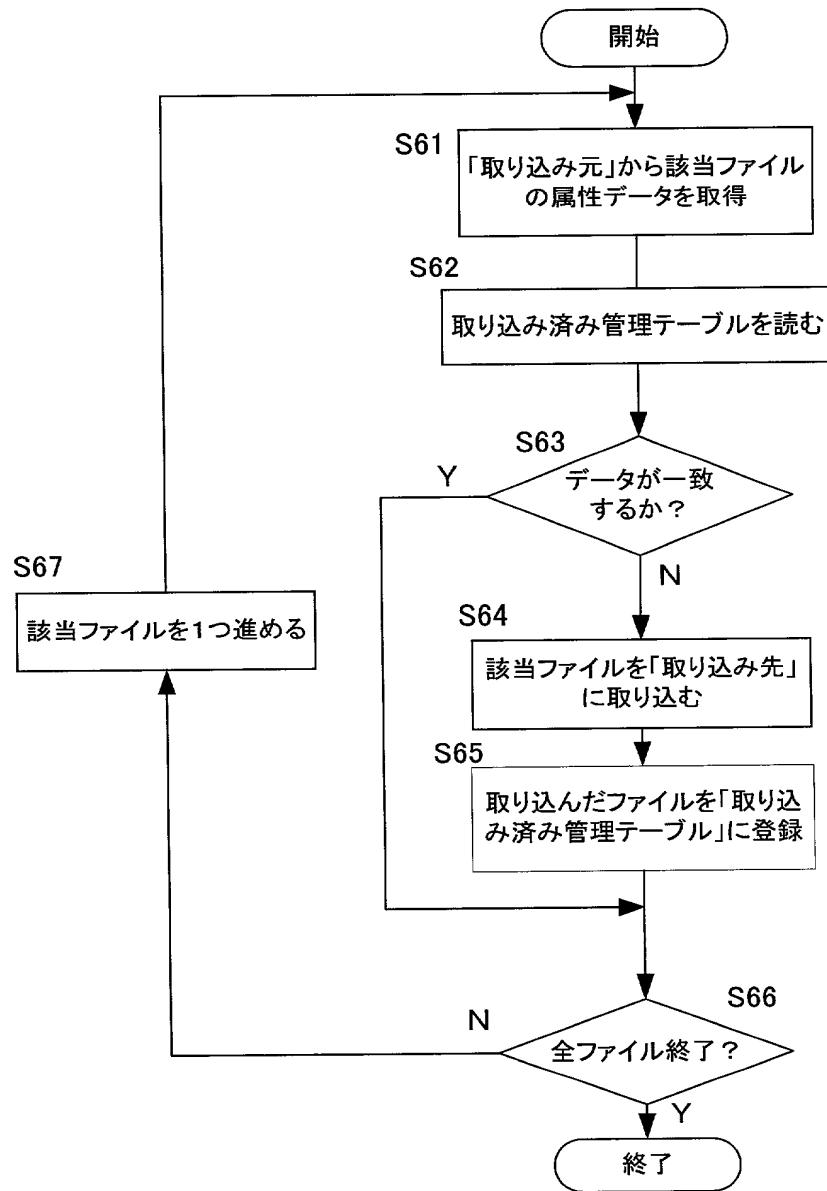


FIG. 7

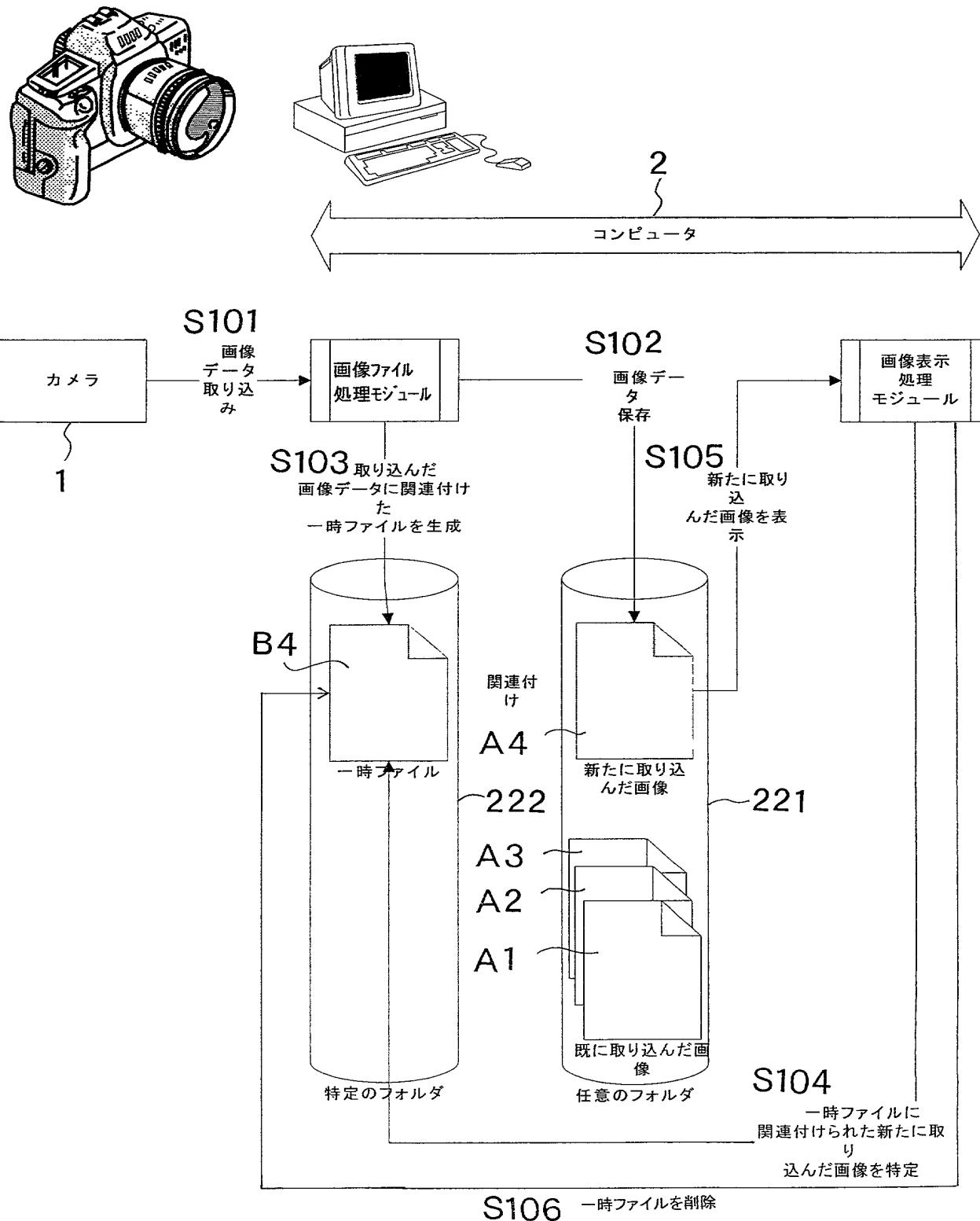


FIG.8

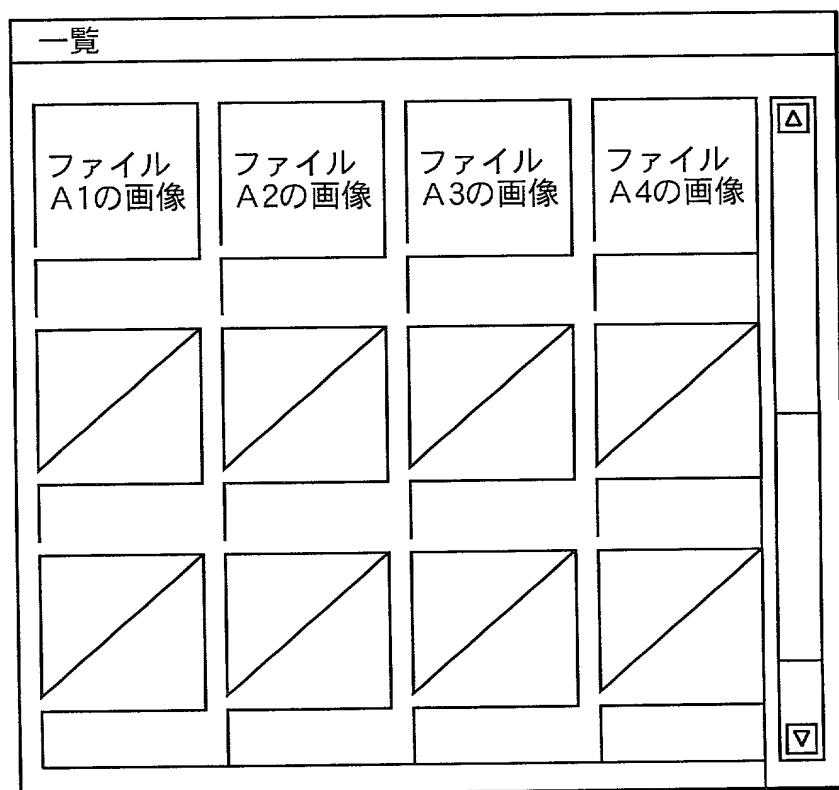


FIG. 9

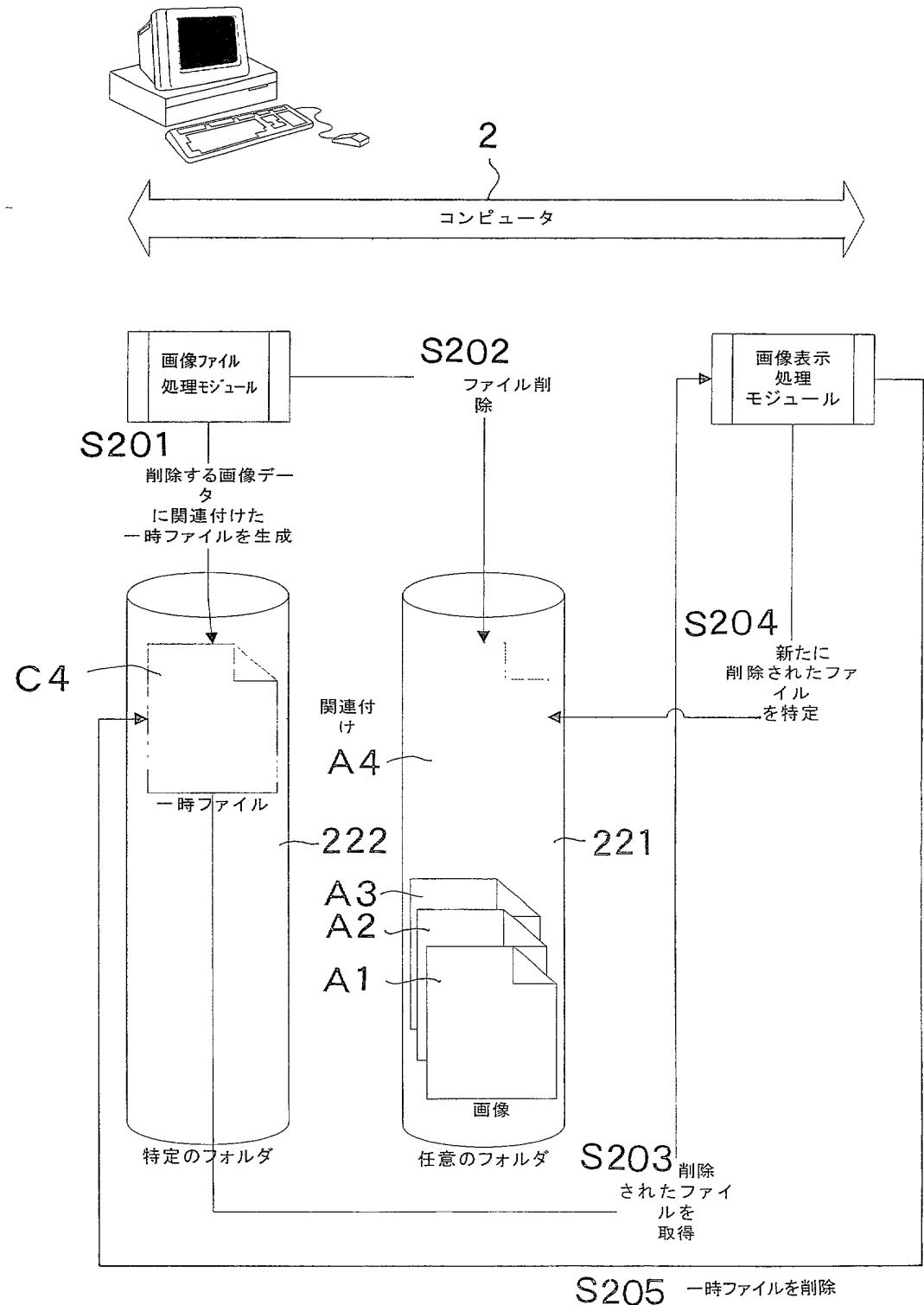


FIG.10

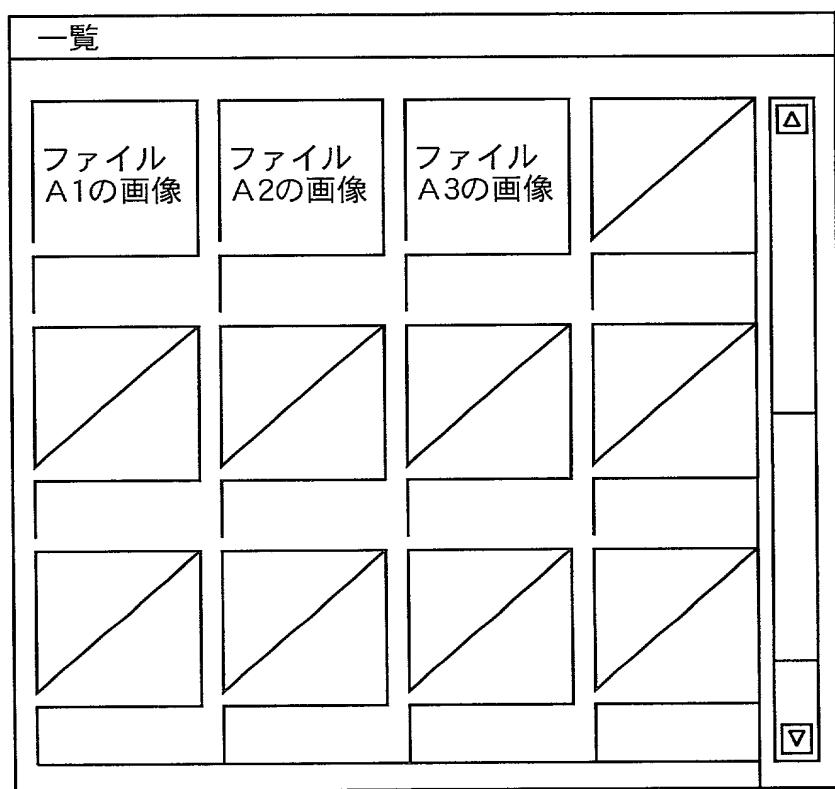


FIG.11

	140		
	141	142	
			150
DSCN0004. JPG		2000/02/25	14:05:00
DSCN0005. JPG		2000/02/25	14:05:00
DSCN0006. JPG		2000/02/25	14:05:01
DSCN0007. JPG		2000/02/25	14:05:01

FIG. 12

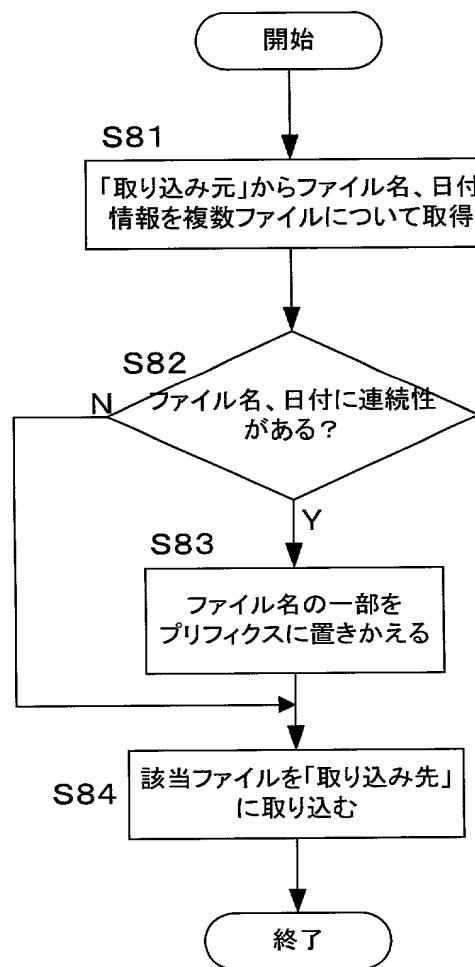


FIG.13

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}

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2000/04/04 16:30:**

FIG. 14

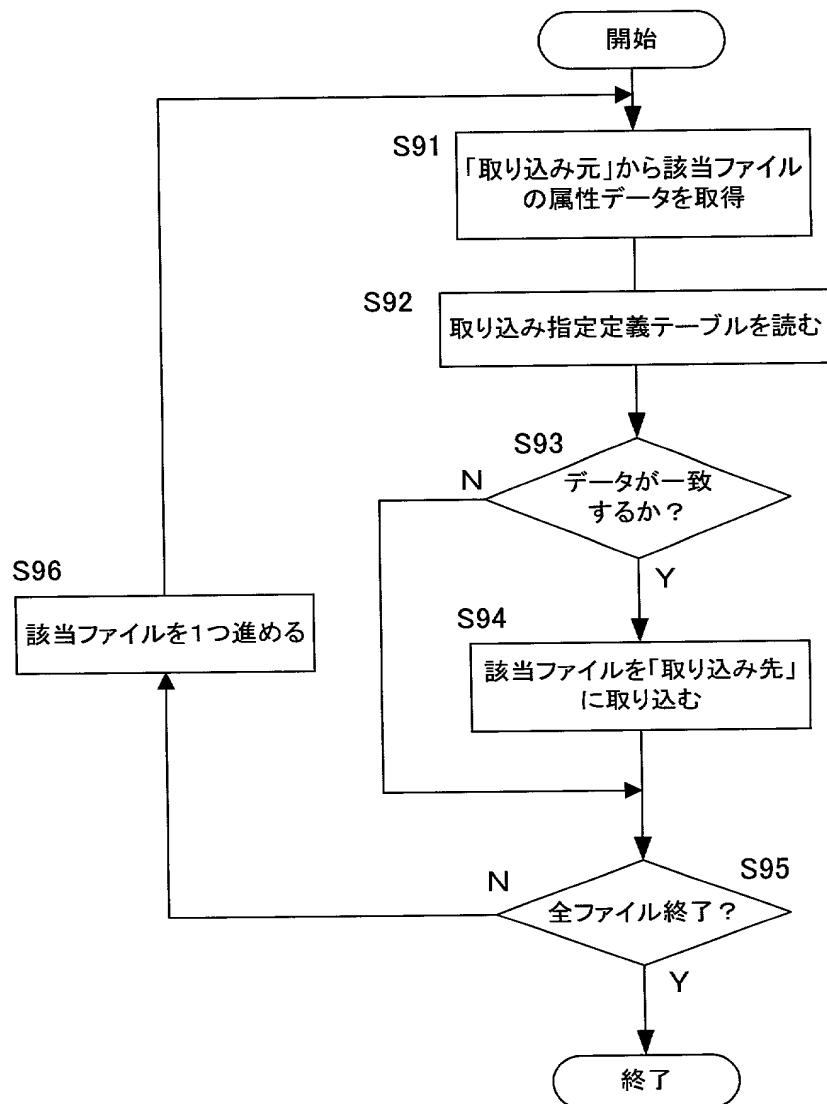


FIG. 15

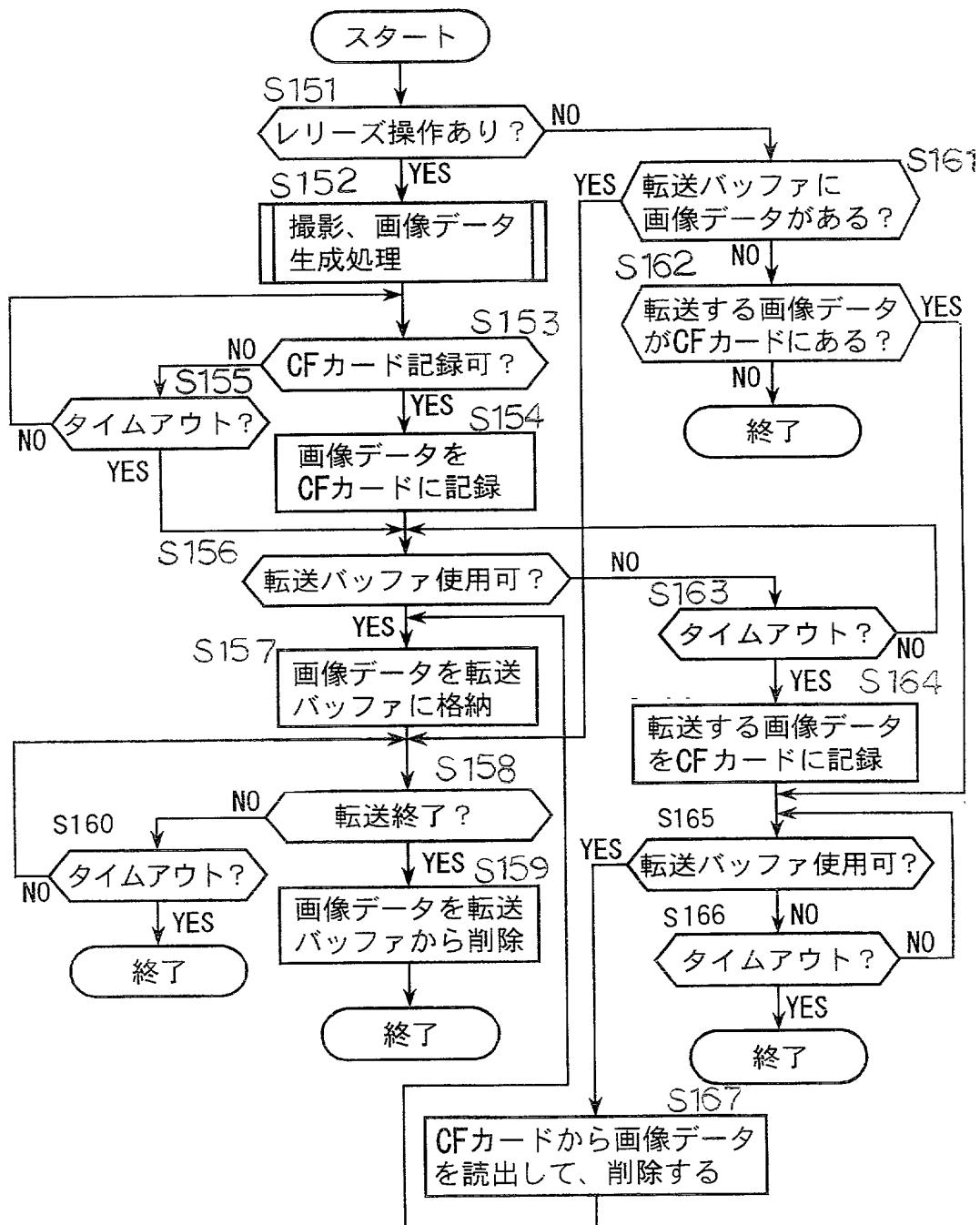


FIG. 16

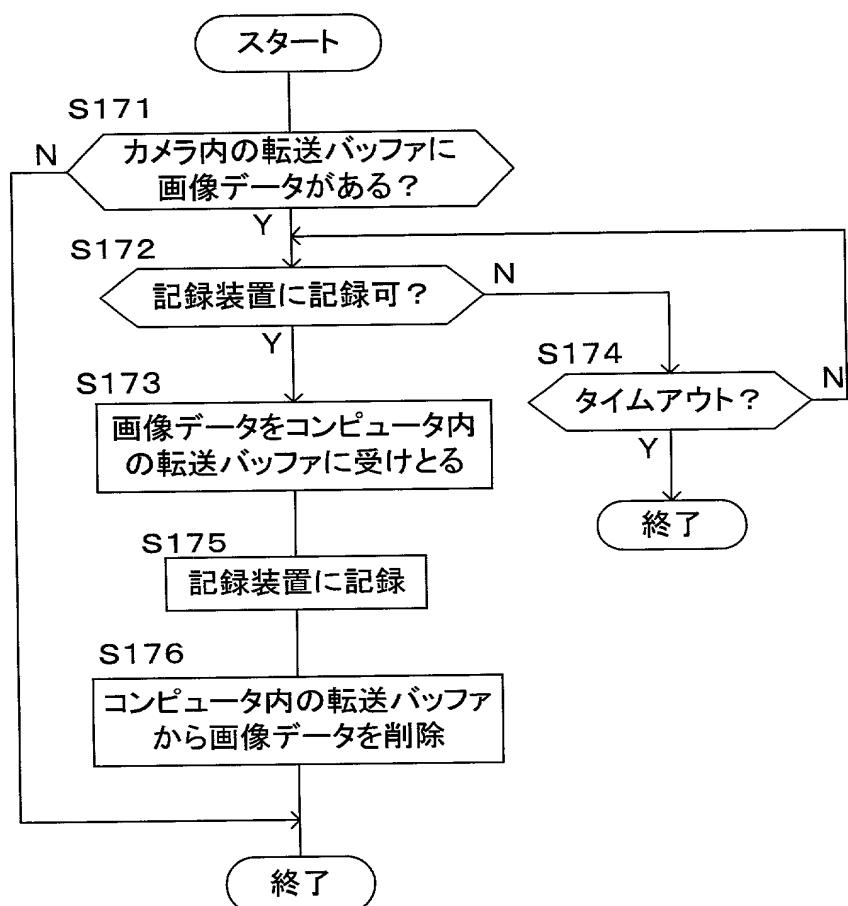


FIG. 17

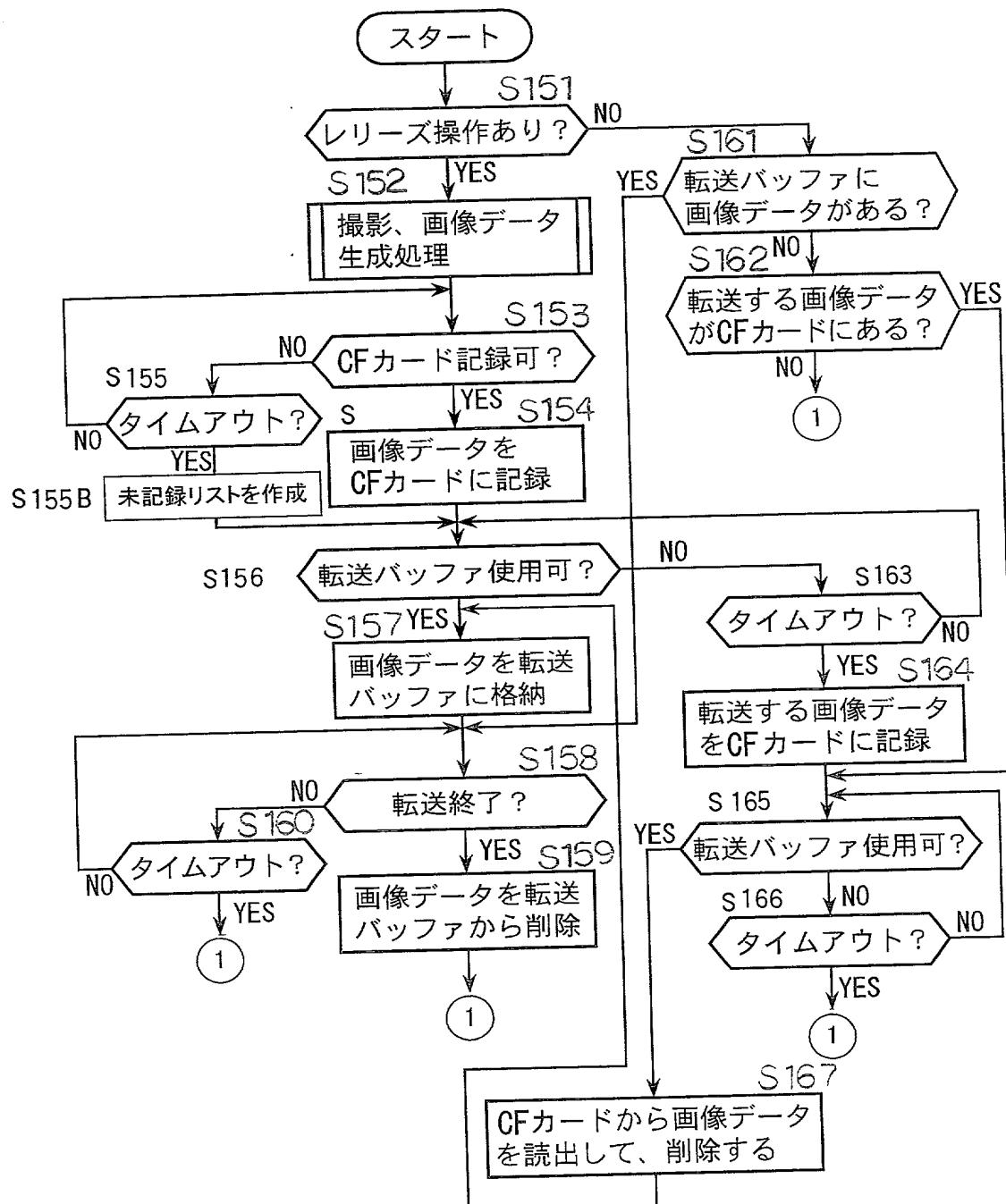


FIG.18

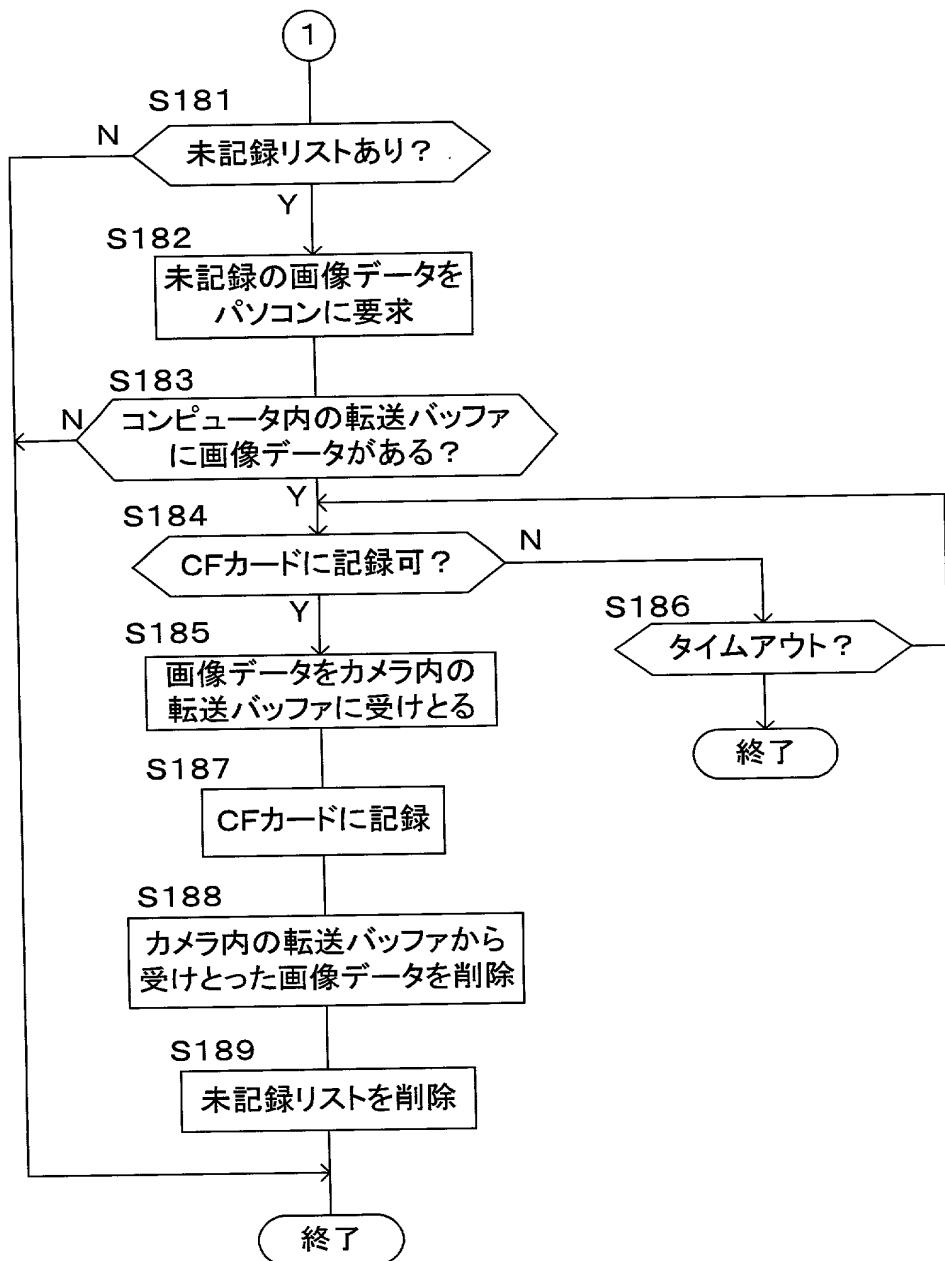


FIG. 19

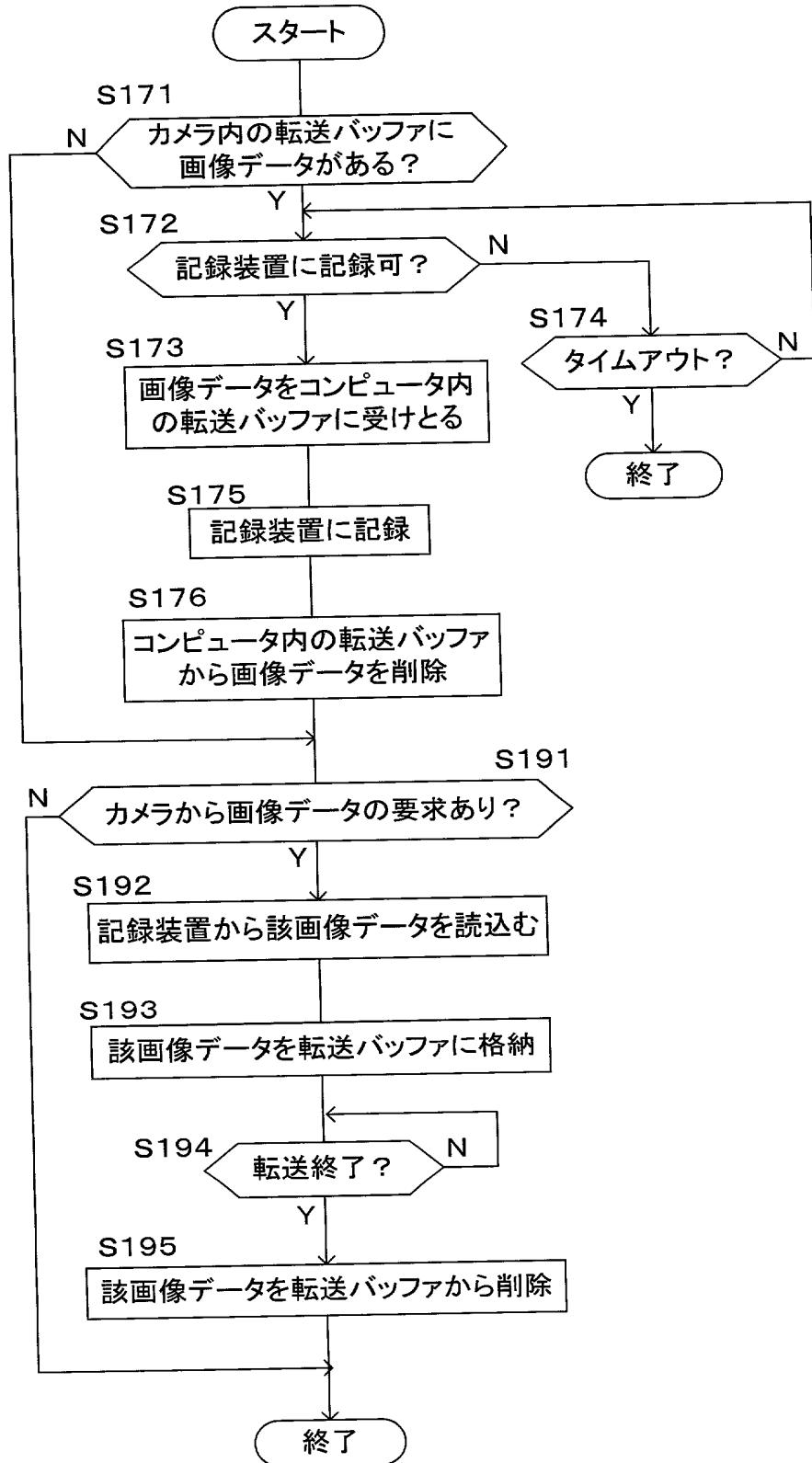
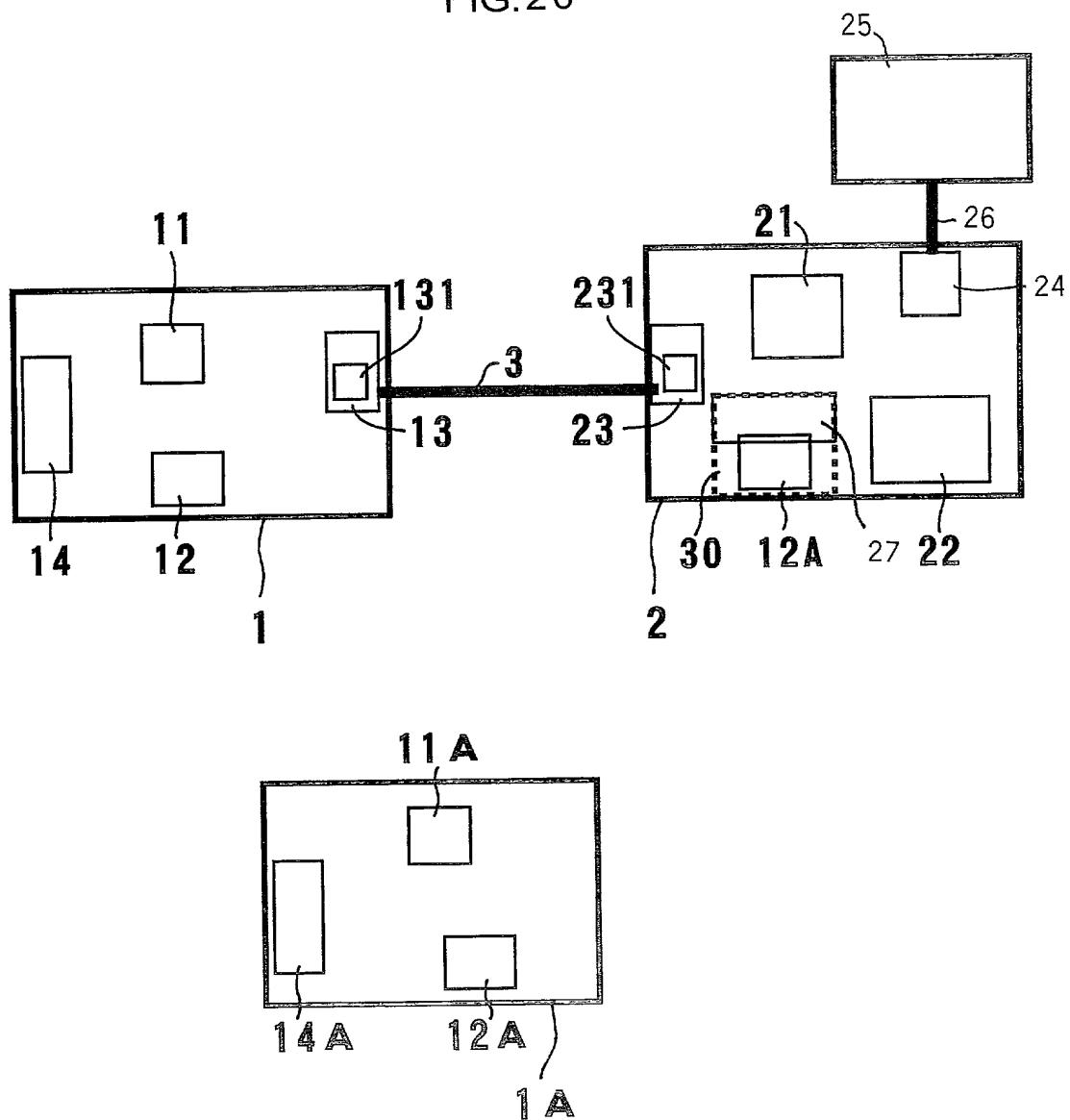


FIG.20



1, 1A	電子スチルカメラ	2	パソコン
11, 11A	MPU	2 1	MPU
12, 12A	CFカード	2 2	記録装置
13	インターフェイス回路	2 3	インターフェイス回路
14, 14A	撮像素子	3 0	スロット
131	転送用バッファメモリ	2 3 1	転送用バッファメモリ

FIG. 21

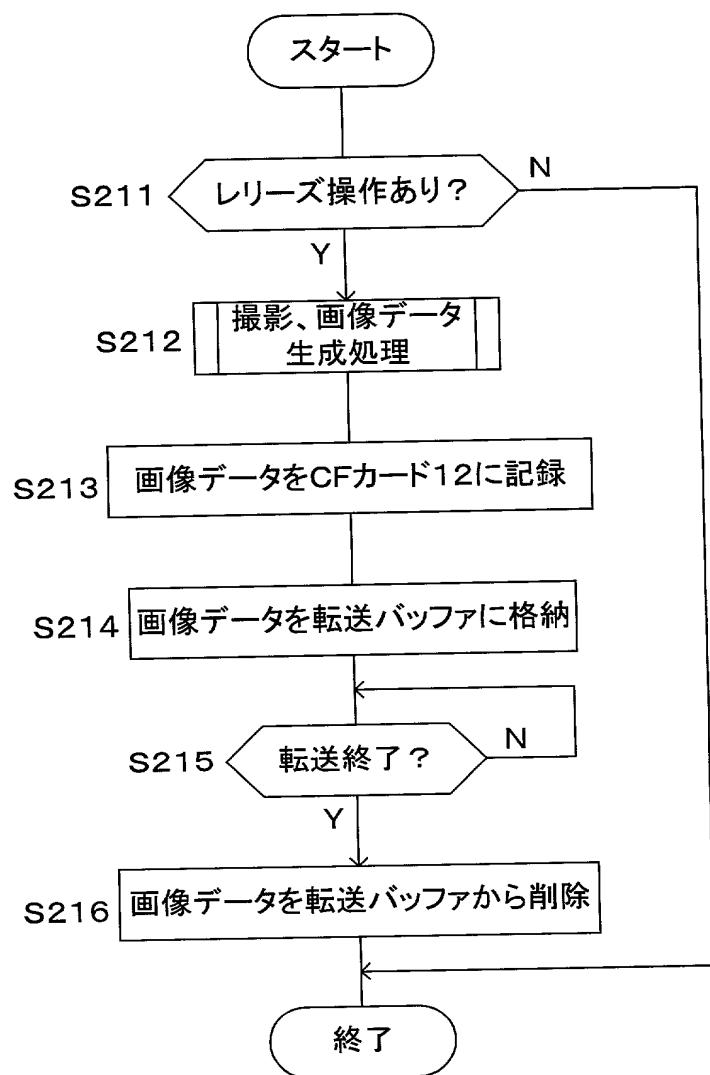


FIG. 22

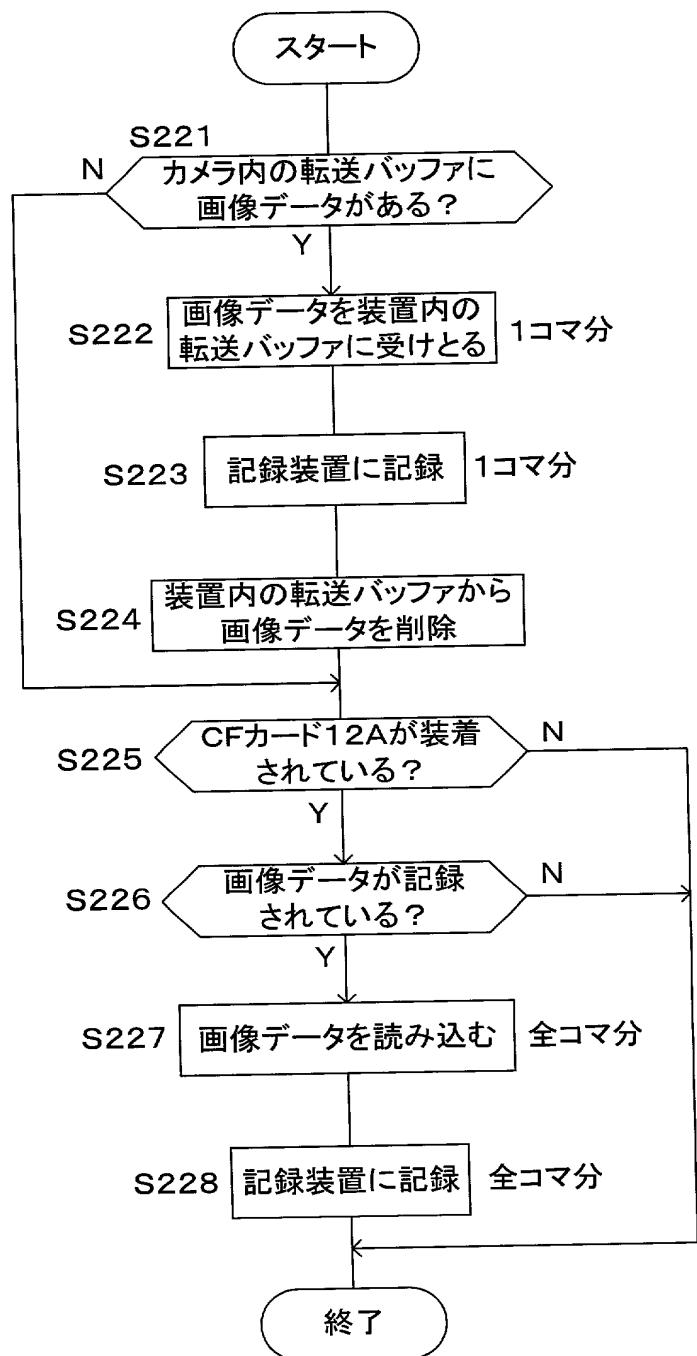


FIG. 23

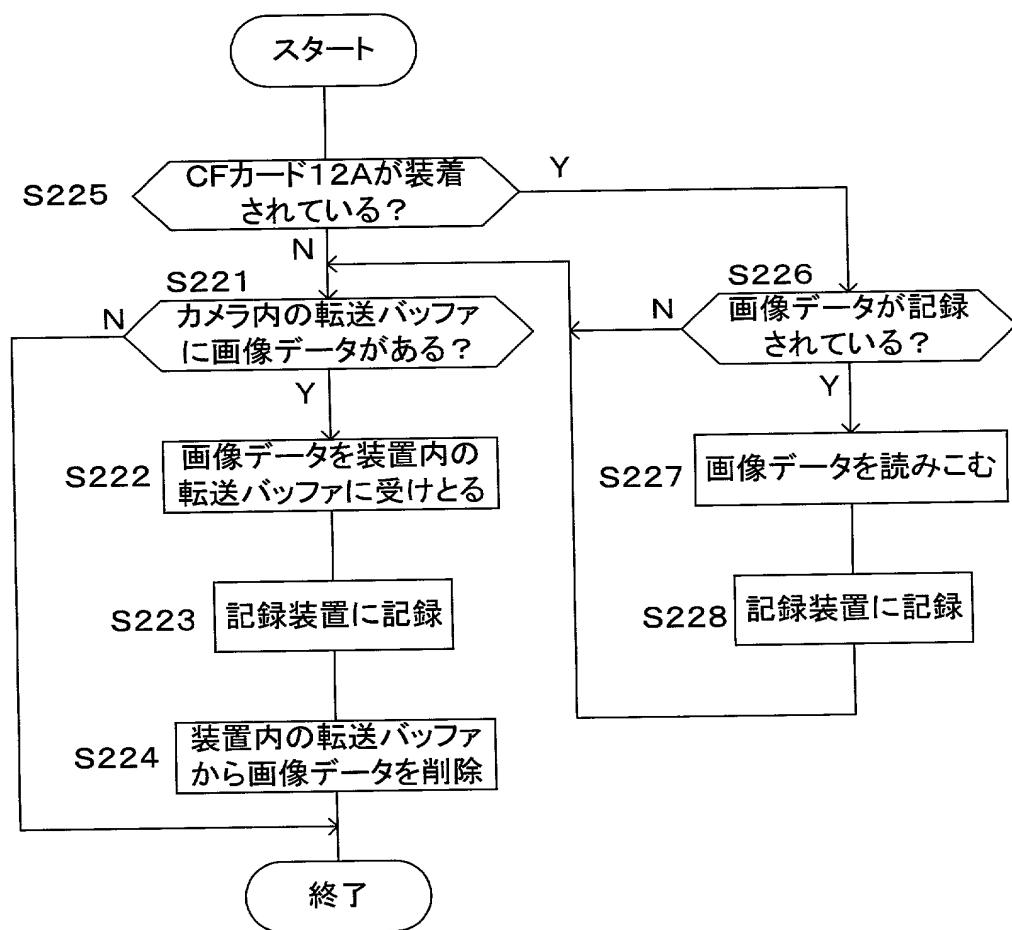


FIG. 24A

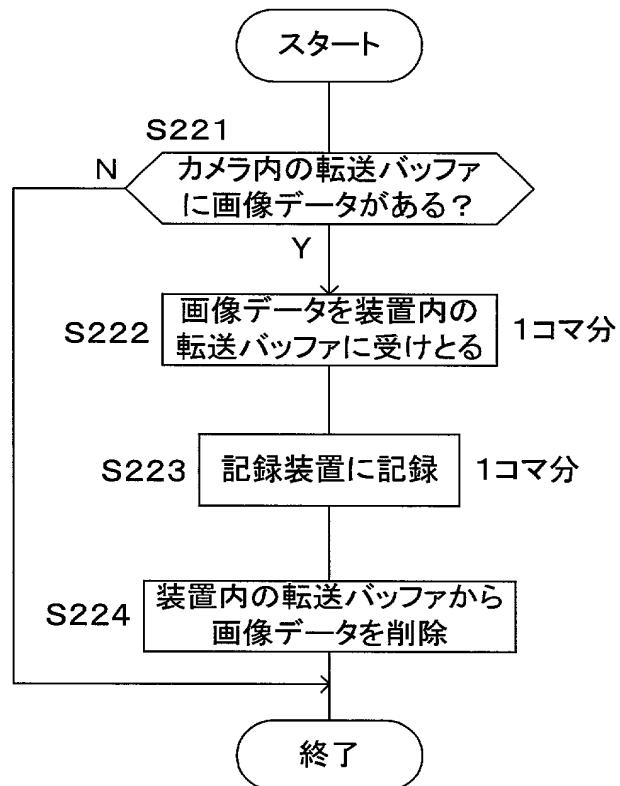


FIG. 24B

